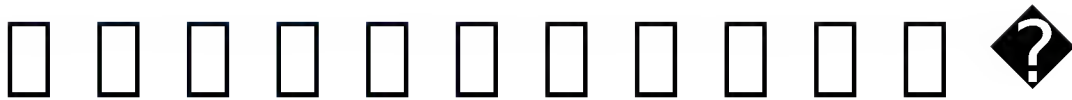
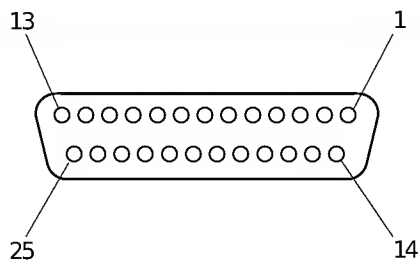


HITACHI FLORA



FLORA

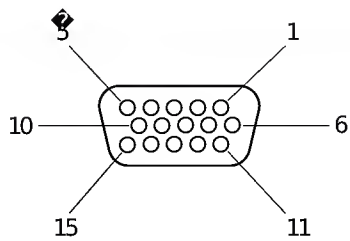


Pin	Signal	Direction	Notes
1	STROBE-N	DATA0	DATA7
2	DATA0	DATA0	DATA7
3	DATA1		
4	DATA2		
5	DATA3		
6	DATA4		
7	DATA5		
8	DATA6		
9	DATA7		
10	ACKLG-N	ACK	
11	BUSY	BUSY	
12	PE	PE	
13	SLCT	SLCT	
14	AUTO FD-N	AUTO FD	
15	ERROR-N	ERROR	
16	INT-N	INT	
17	SLCT1-N	SLCT1	
18	25	GND	

Pin 1 : DATA0, Pin 2 : DATA1, Pin 3 : DATA2, Pin 4 : DATA3, Pin 5 : DATA4, Pin 6 : DATA5, Pin 7 : DATA6, Pin 8 : DATA7, Pin 9 : DATA8, Pin 10 : DATA9, Pin 11 : DATA10, Pin 12 : DATA11, Pin 13 : DATA12, Pin 14 : DATA13, Pin 15 : DATA14, Pin 16 : DATA15, Pin 17 : DATA16, Pin 18 : DATA17, Pin 19 : DATA18, Pin 20 : DATA19, Pin 21 : DATA20, Pin 22 : DATA21, Pin 23 : DATA22, Pin 24 : DATA23, Pin 25 : DATA24

ヒント

1 - N : DATA0 - DATA24
25 : GND



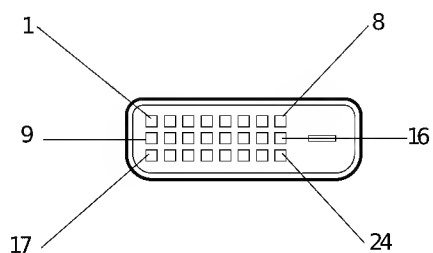
Pin	Signal	Color	Level
1	R	Red	5V
2	G	Green	5V
3	B	Blue	5V
4	NC		—
5	GND	Ground	—
6	R-GND	Red Ground	—
7	G-GND	Green Ground	—
8	B-GND	Blue Ground	—
9	+5V	5V	5V
10	GND	Ground	—
11	NC		—
12	SDA	SDA	5V
13	HSYNC	HSYNC	5V
14	VSYNC	VSYNC	5V
15	SCL	SCL	5V

ヒント

0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0

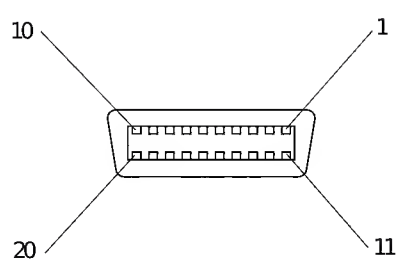


DM 111111



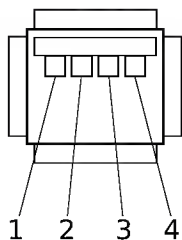
Pin	Signal	Signal
1	T.M.D.S. Data2-	T.M.D.S. 2-
2	T.M.D.S. Data2+	T.M.D.S. 2+
3	T.M.D.S. Data2 SHIELD	T.M.D.S. 2 SHIELD
4	Reserve	
5	Reserve	
6	DDC Clock	DDC
7	DDC Data	DDC
8	No Connect	
9	T.M.D.S. Data1-	T.M.D.S. 1-
10	T.M.D.S. Data1+	T.M.D.S. 1+
11	T.M.D.S. Data1 SHIELD	T.M.D.S. 1 SHIELD
12	Reserve	
13	Reserve	
14	+5V Power	+5V
15	Ground(for +5V)	(+5V)
16	Hot Plug Detect	
17	T.M.D.S. Data0-	T.M.D.S. 0-
18	T.M.D.S. Data0+	T.M.D.S. 0+
19	T.M.D.S. Data0 SHIELD	T.M.D.S. 0 SHIELD
20	Reserve	
21	Reserve	
22	T.M.D.S. Clock SHIELD	T.M.D.S. SHIELD
23	T.M.D.S. Clock+	T.M.D.S. +
24	T.M.D.S. Clock-	T.M.D.S. -

DFP



Pin	Signal	Function	Notes
1	TX1 +	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
2	TX1 -	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
3	TX1 SHIELD	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
4	TXC SHIELD	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
5	TXC +	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
6	TXC -	TMS (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
7	GND	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
8	+5VDC	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
9	Reserved	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
10	Reserved	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
11	TX2 +	TMS (R) (R) (R) (R) (R) (R) (R) (R) (R) (R)	00
12	TX2 -	TMS (R) (R) (R) (R) (R) (R) (R) (R) (R) (R)	00
13	TX2 SHIELD	TMS (R) (R) (R) (R) (R) (R) (R) (R) (R) (R)	—
14	TX0 SHIELD	TMS (R) (R) (R) (R) (R) (R) (R) (R) (R) (R)	—
15	TX0 +	TMS (B) (B) (B) (B) (B) (B) (B) (B) (B) (B)	00
16	TX0 -	TMS (B) (B) (B) (B) (B) (B) (B) (B) (B) (B)	00
17	Reserved	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	—
18	SENSE	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00
19	DDC / SDA	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	000
20	DDC / SCL	DDC (G) (G) (G) (G) (G) (G) (G) (G) (G) (G)	00

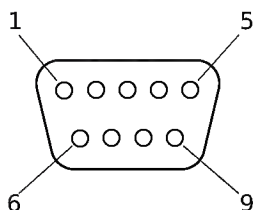
USB



Pin	Signal	Color	Wiring
1	+5V	Green	Green
2	DATA-	Blue	Blue
3	DATA+	Red	Red
4	GND	Black	Black

ヒント

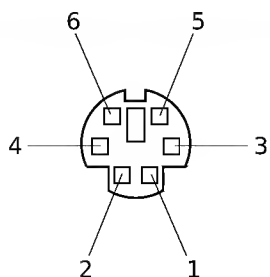
USB Universal Serial Bus



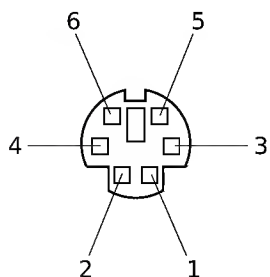
Pin	Signal	Color	Wiring
1	DCD	Green	Green
2	RD	Blue	Blue
3	TD	Red	Red
4	DTR	Black	Black
5	GND	Black	Black
6	DSR	Black	Black
7	RTS	Blue	Blue
8	CTS	Red	Red
9	RI	Blue	Blue

ヒント

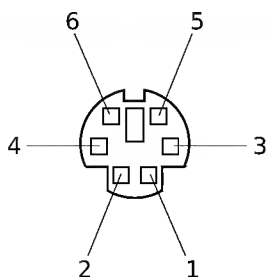
RS-232C



Pin	Signal	Width (mm)	Height (mm)
1	DATA (K/B)	0.127	0.127
2	NC	0.127	—
3	GND	0.127	—
4	+5V *	0.127	0.127
5	Clock (K/B)	0.127	0.127
6	NC	0.127	—

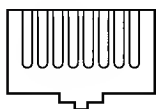


Pin	Signal	Width (mm)	Height (mm)
1	DATA (M5)	0.127	0.127
2	NC	0.127	—
3	GND	0.127	—
4	+5V	0.127	0.127
5	Clock (M5)	0.127	0.127
6	NC	0.127	—

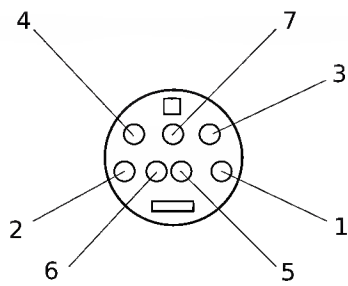


Pin	Signal	Color	Notes
1	DATA (M)	Blue	
2	DATA (K)	Orange	
3	GND	Green	
4	+5V	Red	
5	Clock (M)	Blue	
6	Clock (K)	Orange	

LAN



Pin	Signal	Color	Notes
1	TD+	Blue	
2	TD-	Orange	
3	RD+	Blue	
4	GND	Green	
5	GND	Blue	
6	RD-	Orange	
7	GND		
8	GND		



□ □	□ □ □	□ □
1	GD	—
2	GD	—
3	Y□ □	□ □
4	C□ □	□ □
5	□ □	—
6	□ □	—
7	□ □ □ □ □ □ □	□ □